

- C1*
Cond.
46. A microprocessor chip as recited in claim 45, wherein said clock mode includes a normal clock mode and at least one reduced power clock modes.
47. A microprocessor chip as recited in claim 39,
wherein said microprocessor supports a plurality of reduced power modes, and
wherein over heating of said microprocessor chip is averted by reducing the frequency of the clock signal chip to a value associated with one of the reduced power modes...

R E M A R K S

This Amendment adds new claims 38-47 to the Application. Examination of these additional claims is respectfully requested. Claims 25, 18 and 31 are also canceled from the Application.

Respectfully submitted,



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